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To:	Examiner Fazil Erdem	From:	Steven Fischman
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Phone:		Date:	12/3/2004
Re:	U.S. Serial No: 10/707/863 Group Art Unit: 2826 Confirmation No: 2862 Docket No: BUR920030058US1 (16693)	CC:	


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3. Response Under 37 C.F.R. §§1.111 AND 1.143

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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8) Applicant(s): Ebenezer E. Eshun, et al.			Docket No. BUR920030058US1 (16693)
Application No. 10/707,863	Filing Date January 19, 2004	Examiner Fazli Erdem	Group Art Unit 2826
Invention: HIGH TOLERANCE TCR BALANCED HIGH CURRENT RESISTOR FOR RF CMOS AND RF SiGe BICMOS APPLICATIONS AND CADENCED BASED HIERARCHICAL PARAMETERIZED CELL DESIGN KIT WITH TUNABLE TCR AND ESD RESISTOR BALLASTING FEATURE			
<p>I hereby certify that this <u>RESPONSE UNDER 37 C.F.R. §§1.111 AND 1.143</u> (Identify type of correspondence)</p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>703-872-9306</u>)</p> <p>on <u>December 3, 2004</u> (Date)</p> <p style="text-align: center;"><u>Steven Fischman</u> (Typed or Printed Name of Person Signing Certificate)</p> <p style="text-align: center;"><u></u> (Signature)</p> <p style="text-align: center;">Note: Each paper must have its own certificate of mailing.</p>			

P18/REV02

AMENDMENT TRANSMITTAL LETTER (Large Entity)				Docket No. BUR920030058US1 (16693)	
Applicant(s): Ebenezer E. Eshun, et al.					
Application No. 10/707,863	Filing Date January 19, 2004	Examiner Fazli Erdem	Customer No. 23389	Group Art Unit 2826	Confirmation No. 2862
Invention: HIGH TOLERANCE TCR BALANCED HIGH CURRENT RESISTOR FOR RF CMOS AND RF SiGe BICMOS APPLICATIONS AND CADENCED BASED HIERARCHICAL PARAMETERIZED CELL DESIGN KIT WITH TUNABLE TCR AND ESD RESISTOR BALLASTING FEATURE					
COMMISSIONER FOR PATENTS:					
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	30 -	30 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	6 -	6 =	0 x	\$88.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00
<input checked="" type="checkbox"/> No additional fee is required for amendment. <input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____ <input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed. <input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0456/IBM <input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16. <input checked="" type="checkbox"/> Any patent application processing fees under 37 CFR 1.17. <input type="checkbox"/> Payment by credit card. Form PTO-2038.					
WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.					
 _____ Signature			Dated: December 3, 2004		
Steven Fischman Registration No. 34,594 SCULLY, SCOTT, MURPHY & PRESSER			I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____ _____ (Date) _____ Signature of Person Mailing Correspondence _____ Typed or Printed Name of Person Mailing Correspondence		
CC:					

P11LARGE/REV08

AMENDMENT TRANSMITTAL LETTER (Large Entity)				Docket No. BUR920030058US1 (16693)	
Applicant(s): Ebenezer E. Eshun, et al.					
Application No. 10/707,863	Filing Date January 19, 2004	Examiner Fazli Erdem	Customer No. 23389	Group Art Unit 2826	Confirmation No. 2862
Invention: HIGH TOLERANCE TCR BALANCED HIGH CURRENT RESISTOR FOR RF CMOS AND RF SiGe BICMOS APPLICATIONS AND CADENCED BASED HIERARCHICAL PARAMETERIZED CELL DESIGN KIT WITH TUNABLE TCR AND ESD RESISTOR BALLASTING FEATURE					
<u>COMMISSIONER FOR PATENTS:</u>					
Transmitted herewith is an amendment in the above-identified application.					
The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	30 -	30 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	6 -	6 =	0 x	\$88.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00
<input checked="" type="checkbox"/> No additional fee is required for amendment. <input type="checkbox"/> Please charge Deposit Account No. _____ In the amount of _____ <input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed. <input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0456/IBM <input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16. <input checked="" type="checkbox"/> Any patent application processing fees under 37 CFR 1.17. <input type="checkbox"/> Payment by credit card. Form PTO-2038.					
WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.					
 Steven Fischman Registration No. 34,594 SCULLY, SCOTT, MURPHY & PRESSER			Dated: December 3, 2004		
CC:			I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 4450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____ _____ (Date) _____ Signature of Person Mailing Correspondence _____ Typed or Printed Name of Person Mailing Correspondence		

P11LARGE/REV08

DEC 03 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**Applicant:** Ebenezer E. Eshun, et al.**Examiner:** Fazli Erdem**Serial No.:** 10/707,863**Art Unit:** 2826**Filed:** January 19, 2004**Docket:** BUR920030058US1 (16693)**For:** HIGH TOLERANCE TCR BALANCED
HIGH CURRENT RESISTOR FOR RF CMOS
AND RF SiGe BiCMOS APPLICATIONS AND
CADENCED BASED HIERARCHICAL
PARAMETERIZED CELL DESIGN KIT WITH
TUNABLE TCR AND ESD RESISTOR
BALLASTING FEATURE**Dated:** December 3, 2004**Confirmation No:** 2862Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE UNDER 37 C.F.R. §§1.111 AND 1.143**

Sir:

In response to the Office Action dated November 3, 2004, applicants provisionally elect, without traverse, Group I, Claims 1-12, which is directed to a semiconductor device, for continued prosecution in the above-identified patent application. Furthermore, in light of the species restriction under requirement of Group III, Claims 24-30, applicants respectfully submit that because these claims are not elected for continued prosecution, election of a species requirement is obviated.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: December 3, 2004
Steven Fischman